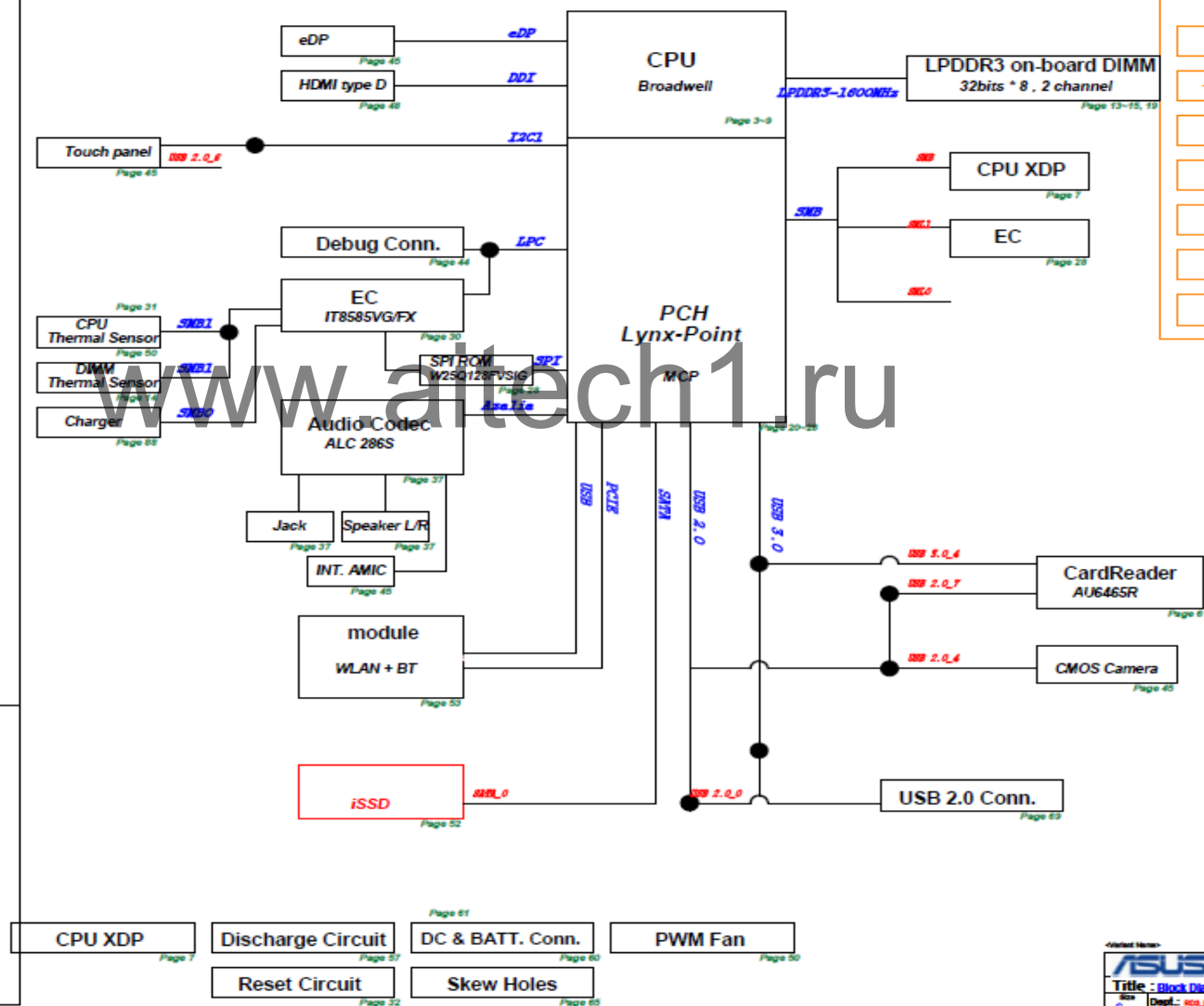


## BLOCK DIAGRAM

SYSTEM PAGE REF.

PAGE	Content
1	Block Diagram
2	System Setting
3	CPU_DISPLAY
4	CPU_DDR3L
5	CPU_LPC, SPI, SMB, CLINK
6	CPU_POWER
7	CPU_XDP
8	CPU_MISC, JTAG, CLK
9	CPU_CFG, RSVD
13	DDR3L_TERMINATION
14	DDR3L_ON-BOARD_A(1)
15	DDR3L_ON-BOARD_A(2)
19	DDR3L_CA_DQ VOLTAGE
20	CPU_PCH_EDP SB, PCI, DISPLAY
21	CPU_PCH_CGP10, LP10, MISC
22	CPU_PCH_SATA, JTAG, AUDIO, RTC
23	CPU_PCH_PCIE, USB
24	CPU_PCH_CLOCK SIGNALS
25	CPU_PCH_SYS_POWER
26	CPU_PCH_POWER, GND
27	CPU_PCH_POWER, GND
28	PCH_SPI ROM, OTH
30	KBC_IT8888
31	EC_KB
32	RST_Reset Circuit
36	AUD-PHONE JACK
37	AUD-ALC3236-CG
40	CB_GL3233E (EMMC)
41	CB_AU6465
44	BUG_Debug
45	CRT_LCD Panel_CMOS_MIC
48	HDMI_type-D
50	FAN_Thermal Sensor
52	USB3.0 PORT
53	PCI_Wlan & BT COMBO
54	XDD_SSD
55	SATA re-driver
57	DSG_Discharge
58	FRO_PROTECT
59	Device Power Switch
60	DC_DC & BAT Conn.
61	Sensors
63	IO_BTN_BD & LID SW
65	ME_Conn & Skew Hole
66	BD to BD Conn
68	EMI & RF
69	KBC_EC PWR Switch
<hr/>	
80	PW_+VCORE (NCP81101A)
81	PW_+3VADSWH+5VSUS (TPS51275)
82	PW_1.05VSUS (TPS51362)
83	PW_+1.35V/+0.675V (RT8207M)
84	PW_+1.5V (RT8068A)
86	PW_BackLight(RT8532)
88	PW_CHARGER(HPA02196RGRR)
90	PW_Power Limit
91	PW_Load Switch

### T300 air Broadwell Platform (Y-Processor)



## T300air Block Diagram

## Connected Standby

## Power

+VCCIN

+3VADSW/+

1.05VSI

+1.2V / +0

+1.5V!

Charg

### Load Switch

---

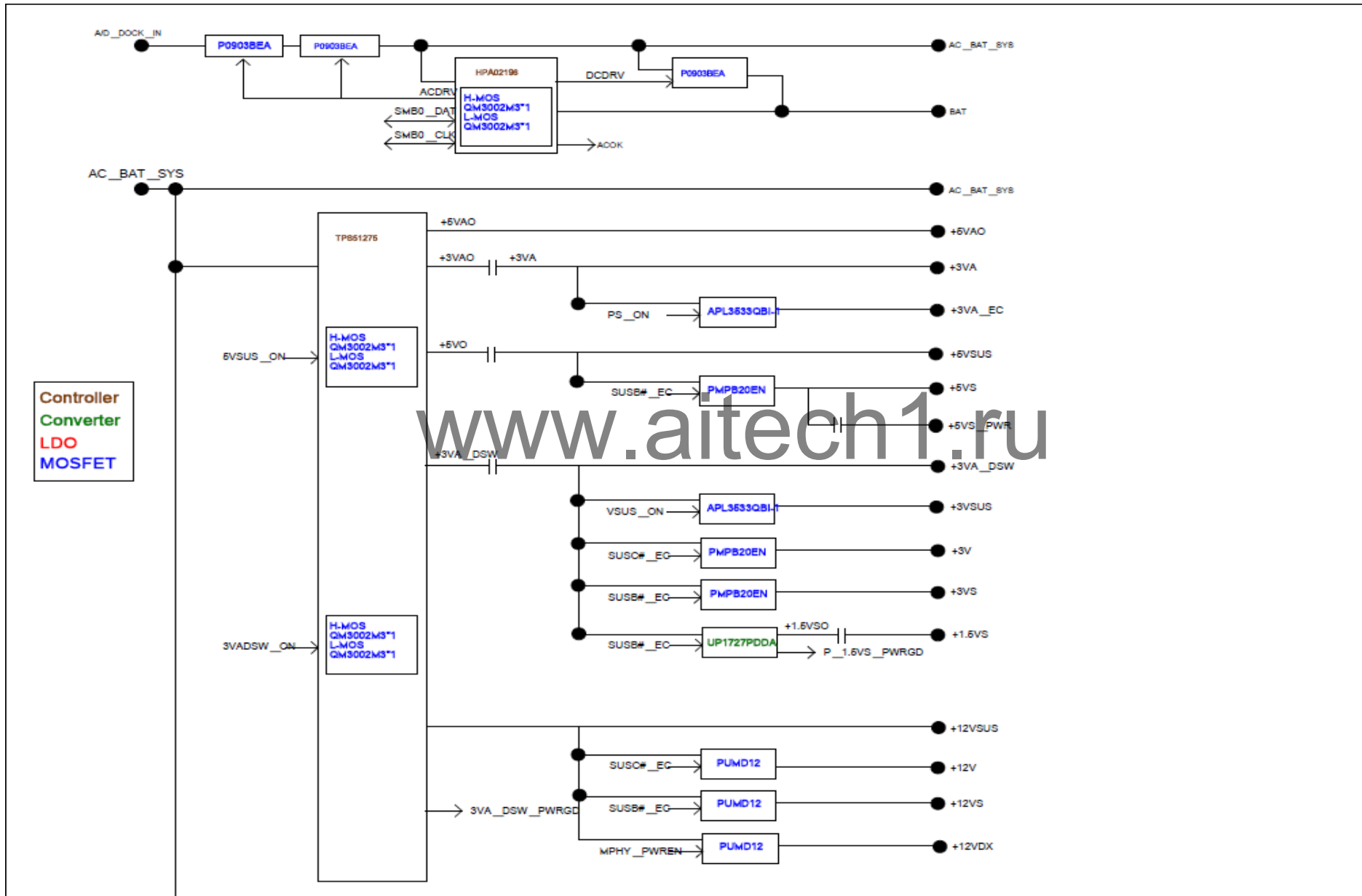
1

61

Project Name  
**r300chi**

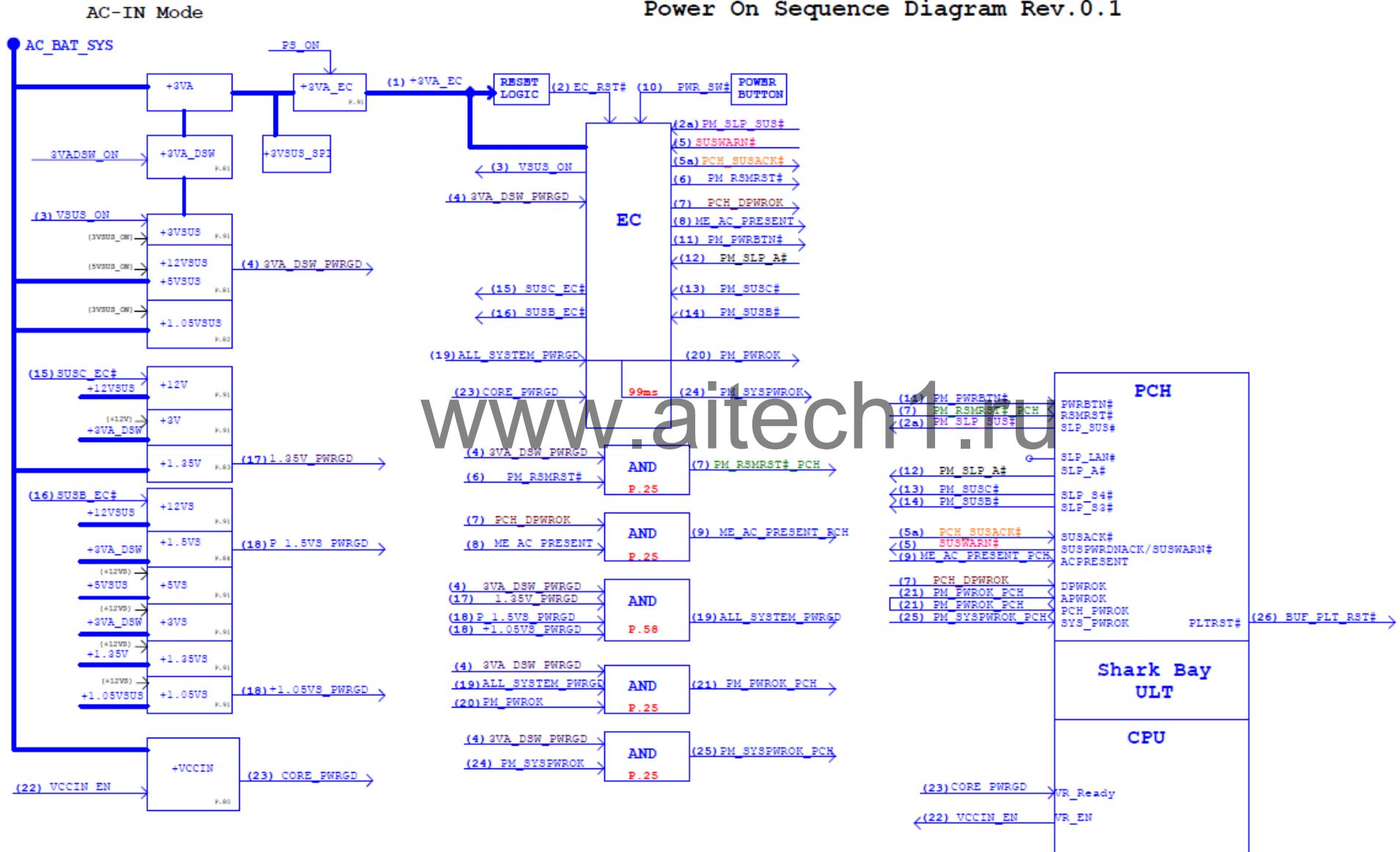
[LJ63](#)
[Engl](#)

# POWER FLOW



# POWER ON SEQUENCE

Power On Sequence Diagram Rev.0.1



# AC POWER ON SEQUENCE

## Power-On Sequence Timing Diagram Rev.0.1

AC-IN Mode



# DC POWER ON SEQUENCE

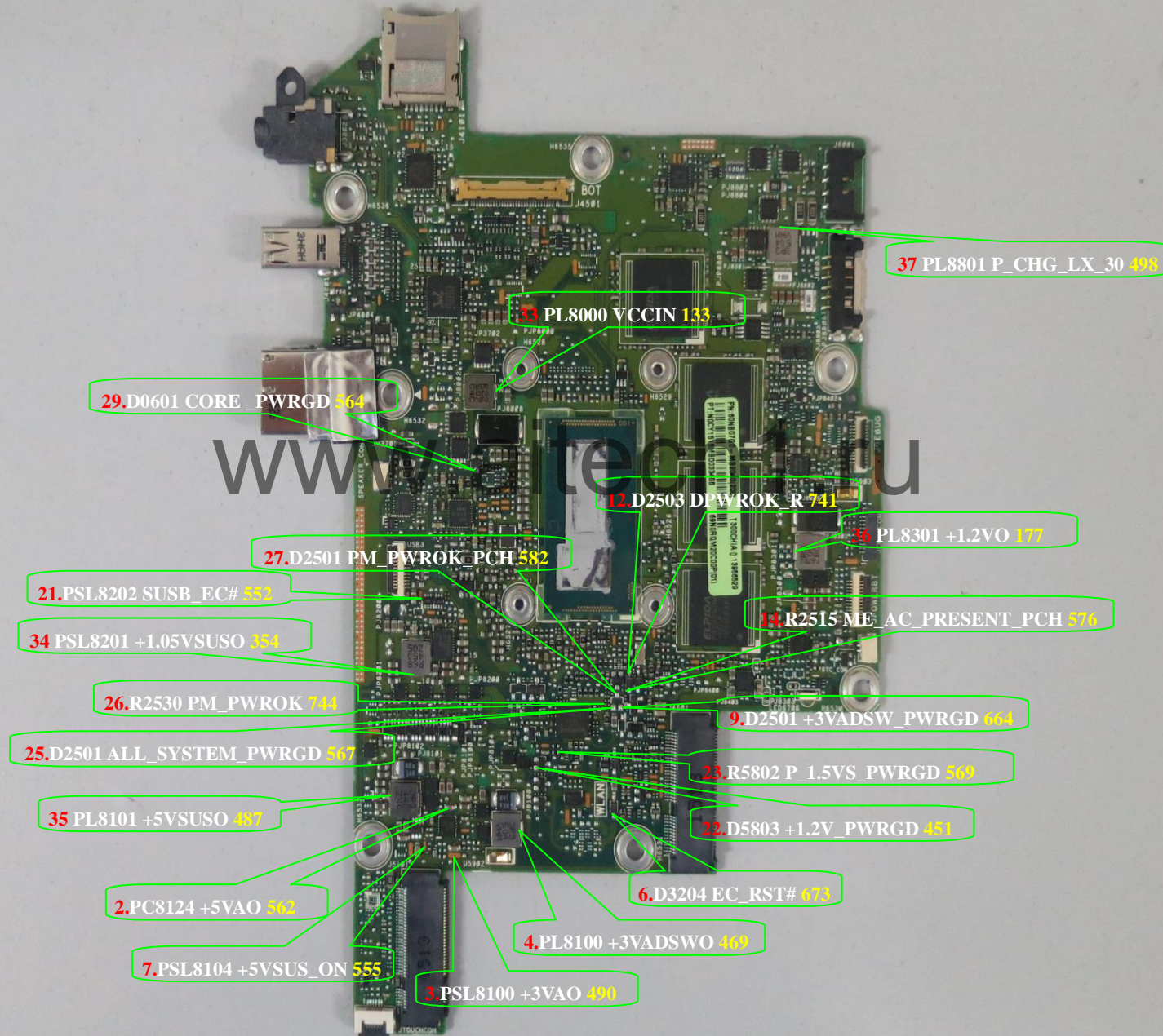
## Power-On Sequence Timing Diagram Rev.0.1

DC-IN Mode



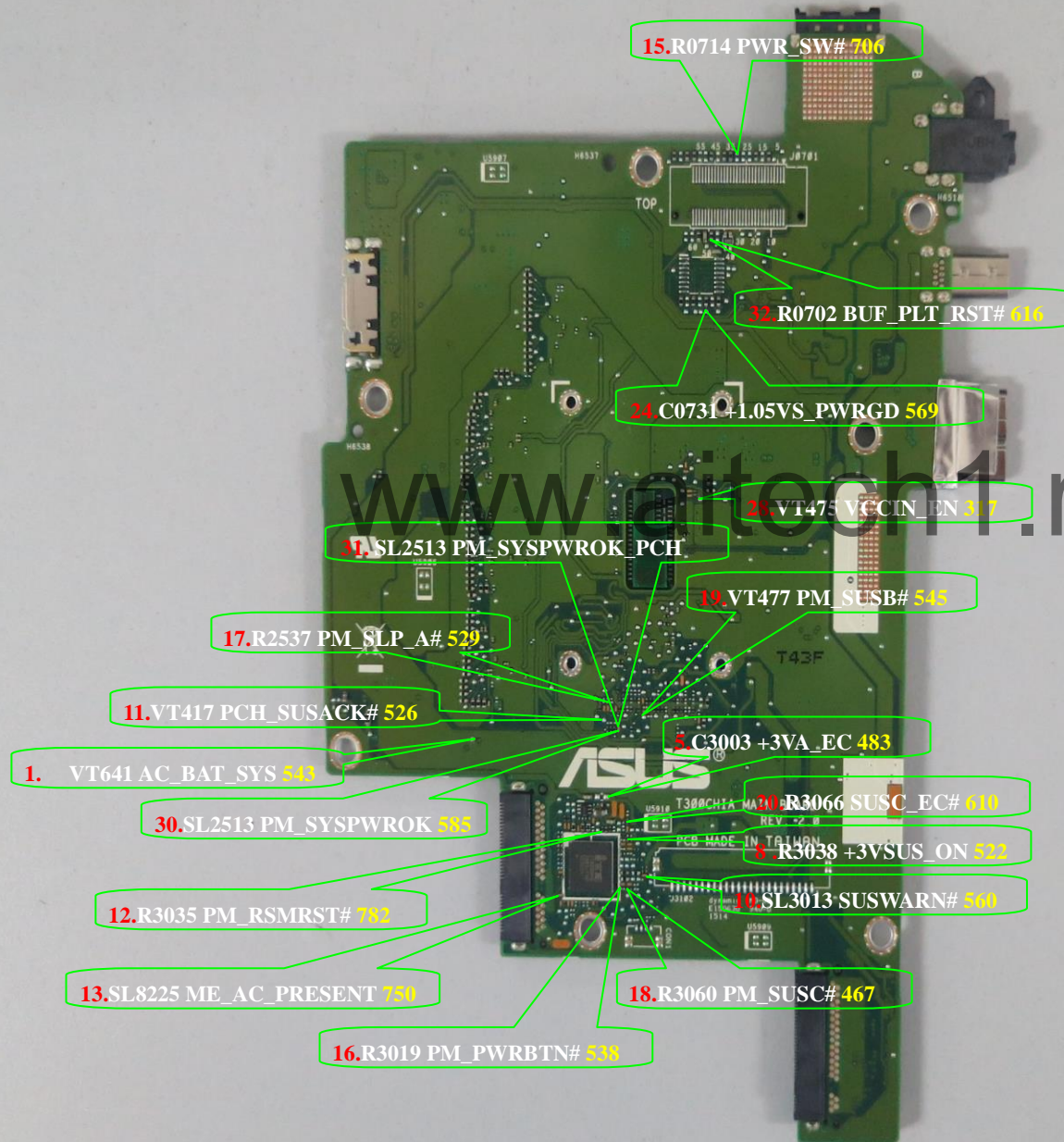


## Signal Measure Point-Bottom



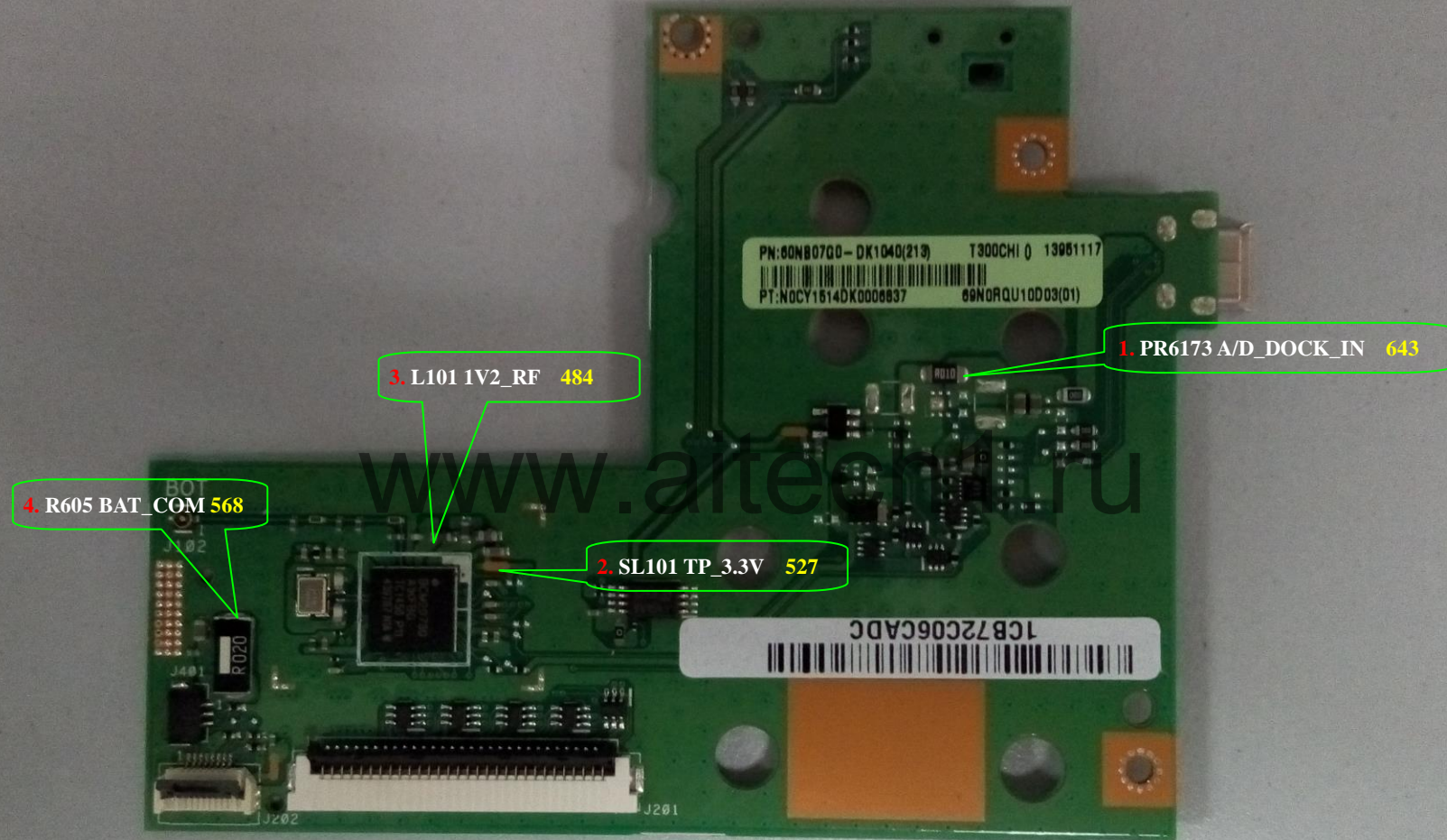


## Signal Measure Point-Top





## Signal Measure Point-Docking Bottom





## Signal Measure Point-Docking Top

